



21st IEEE Symposium on Computer Arithmetic

ARITH 21

April 7-10, 2013

AT&T Conference Center, Austin, Texas, USA

www.arithsymposium.org

CALL FOR PAPERS

Authors are invited to submit papers describing recent advances on all aspects of computer arithmetic, including, but not restricted to the following topics:

- Foundations of number systems and arithmetic
- Arithmetic processor designs and implementations
- Arithmetic algorithms and their analysis
- Highly parallel arithmetic units and systems, including multicore
- Floating-point units and systems
- Standards for number representation and arithmetic
- Impact of high-level languages on arithmetic systems
- Special function implementations
- Low-power computer arithmetic
- Industrial implementations of arithmetic units
- Test and verification techniques for computer arithmetic implementations
- New approaches to fault- and error-tolerance in arithmetic implementations
- Arithmetic for FPGA's and configurable logic
- Design automation for computer arithmetic implementations
- Applications of computer arithmetic:
DSP, Multimedia, Graphics, GPUs, Cryptography, Data compression, etc.

For ARITH-21, we broaden the scope of submissions on computer arithmetic applications and encourage submissions of papers that showcase application-specific optimizations in the design and use of computer arithmetic.

CONFERENCE WEB SITE

Conference web site and detailed instructions on paper submission at:

www.arithsymposium.org

IMPORTANT DATES

Deadline for abstract submission: **Aug. 31, 2012**
Deadline for paper submission: **Sept. 10, 2012**
Acceptance notification: Dec. 2012

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